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ATEEDA founder member of Synopsys's HSpice Integrator Program

Synopsys' HSPICE® Integrator Program promotes integration between Synopsys' HSPICE simulation solution and other EDA products

Edinburgh, UK —March 12, 2008 ATEEDA has become a founder member of Synopsys's HSpice Integrator Program. The HSPICE Integrator Program enables qualified EDA vendors to integrate their products with the de facto standard HSPICE simulator, HSPICE RF simulator, and WaveView Analyzer™. In addition, qualified HSPICE Integrator Program members have access to HSPICE integrator application programming interfaces (APIs). Collaboration between HSPICE Integrator Program members will enable customers to achieve more thorough design verification in a shorter period of time from the improvements offered by inter-company EDA design solutions.

ATEEDA's CEO, David Hamilton said, "ATEEDA is committed to delivering the smoothest mixed signal test design experience possible. An important part of this is ensuring we have the best integration possible of our EDA tool OptimATE with world leading simulation tools. The HSpice Integrator Program is the perfect means to achieve this with Synopsys"

ATEEDA, founded in 2006, specializes in testing mixed signal devices which have both analog and digital sections. The company has developed the world's first effective software electronic design automation (EDA) tool, OptimATE, that allows analog sections to be tested on digital testers, enabling manufacturers to save substantial amounts of time and money.

OptimATE is ATEEDA's core product, offering a unique combination of features and benefits, particularly for mixed signal semiconductor manufacturers. Benefits include dramatically reduced production test time, reduced and test cost, simplified analog test equipment requirements, and improved statistical information about test coverage assisting with reliability.

About ATEEDA

The company is a world leader in developing semiconductor test solutions. It was founded in 2006 to meet a growing industry need to find an efficient and cost-effective way to test the analog sections of mixed signal integrated circuits while enhancing reliability. These circuits are used in almost all consumer products and the automotive industry. ATEEDA is the first company to develop an effective test solution for the analog section of mixed signal devices that can be run on a digital tester. ATEEDA's core product, OptimATE®, is at an advanced stage of mass production validation by several major integrated device manufacturers including automotive industry suppliers.

For further information on this release please visit our website or contact:

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